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### WO/2010/117928 DATA STRIPING IN A FLASH MEMORY DATA ST...

[www.wipo.int/patentscope/search/en/WO/2010/117928](http://www.wipo.int/patentscope/search/en/WO/2010/117928)

Oct 14, 2010 – A method is disclosed for **striping** data from a host (106, 350, 551) to a data storage device (100, 302, 501) that includes a plurality of ...

### Forward error correction - Wikipedia, the free encyclopedia

[en.wikipedia.org/wiki/Forward\\_error\\_correction](http://en.wikipedia.org/wiki/Forward_error_correction)

In telecommunication, information theory, and coding theory, forward error correction (FEC) or ... **Interleaving** FEC coded data can reduce the all or nothing properties of ... Hamming ECC is commonly used to correct NAND **flash** memory errors. ...

### Orthogonal frequency-division multiplexing - Wikipedia, the free ...

[en.wikipedia.org/wiki/Orthogonal\\_frequency-division\\_multiplexing](http://en.wikipedia.org/wiki/Orthogonal_frequency-division_multiplexing)

6.7 Ultra-wideband; 6.8 **FLASH**-OFDM .... However, time **interleaving** is of ...

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### PDF An Adaptive Striping Architecture for Flash Memory Storage Sys...

[www.cis.rich.edu/tw/ischang/papers/RTA902.pdf](http://www.cis.rich.edu/tw/ischang/papers/RTA902.pdf)

File Format: PDF/Adobe Acrobat - Quick View

by LP Chang - Cited by 141 - Related articles

a **striping** architecture. The idea is to use I/O parallelism in speeding up a **flash**-memory storage system. As astute readers may notice, such an approach in I/O ...

### Current Trends in Flash Memory Technology

[www.explore.ieee.org/xpl5/10826/33561/01594704.pdf](http://www.explore.ieee.org/xpl5/10826/33561/01594704.pdf)

by SL Min - 2006 - Cited by 23 - Related articles

certainly not sufficient for high-performance **flash** memory drives. Higher write bandwidth can be obtained by using **interleaving within** a chip or across multiple ...

### Pre-striping Compact Flash Cards at DVinfo.net

[www.dvinfo.net](http://www.dvinfo.net) » ... » Canon EOS 7D for HD

9 posts - 5 authors - Last post: Dec 10, 2010

Hello, not been on the board for a while but I do need to upload the underwater footage our students shot and the projects, will soon! Anyway ...

### Magic Marker Sealing and Striping in Goodlettsville, Tennessee

[magicmarkersealingstriping.com/](http://magicmarkersealingstriping.com/)

Content on this page requires a newer version of Adobe **Flash** Player. ... Sealing and **Striping** is an Asphalt Maintenance company located in Goodlettsville, TN. ...

### An Adaptive Striping Architecture for Flash Memory Storage ...

[www.slideshare.net/Flashdonwithan-adaptive-striping-architecture-fo](http://www.slideshare.net/Flashdonwithan-adaptive-striping-architecture-fo)

The contributance issue of **flash**-memory storage systems with tions of this paper are as follows: a **striping** architecture. The idea is to use I/O parallelism in ...

### Running Tracks, Professional Striping, Flash, Meritt Tennis Court ...

[www.merrittenisandtrack.com/running-tracks.html](http://www.merrittenisandtrack.com/running-tracks.html)

... is a leading tennis court and track construction company in Oklahoma City, Oklahoma. ... running track, synthetic surface installation, professional **striping** ...

IPDF3 [Datelight - NAND Interleaving & Performance - Flash Memory S...](#)  
[www.flashmemoryjournal.com/English/.../2009/02/12\\_F2E\\_Garvin\\_p...](#)  
File Format: PDF/Adobe Acrobat - Quick View  
Bus Level **Interleaving** • **Interleaving** Between **Flash** Planes ... **Flash** Plane  
**Interleaving** • **IO Interleaving** ... 30% to 80%. • Flexible implementation in  
software ...

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